Appln. No.: Unassigned Docket No.: SNDK.344US0
Title: Flash Controller Cache Architecture
Inventors: Conley et al. Exp. Mail: EV437667717US
Filing Date: Herewith Atty. Tel: (415) 318-1160 Sheet 5 of 11

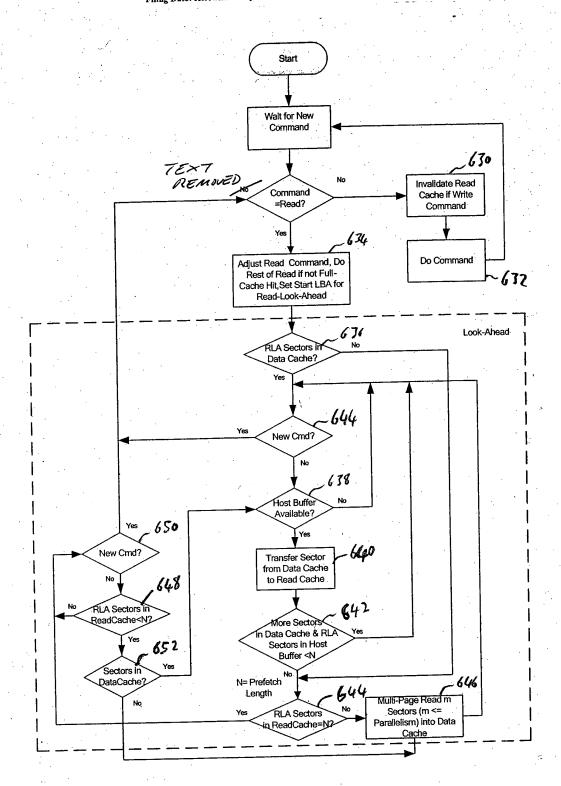


FIGURE 6B FLASH ACCESS MANAGEMENT PROCESS FLOW

III. 2.9 ZOM ROW

Appln. No.: 10/796,575 Docket No.: SNDK.344US0
Title: Flash Controller Cache Architecture

Inventors: Conley et al. Conf. No.: 1109 Group Art Unit: 2818
Filing Date: March 8, 2004 Atty. Tel: (415) 318-1160 Sheet 4 of 11

4/11 Start FIG._6B Wait for New Command 630 Invalidate Read No Command Cache if Write =Read Command 634 Yes Do Command Adjust Read Command, Do Rest of Read if not Full-Cache 632 Hit, Set Start LBA for Read-Look-Ahead Look-Ahead 636 **RLA** No Sectors in Data Cache Yes. 644 Yes New CMD Yes 650 No 638 New CMD Host No **Buffer Available** No 648 640 Yes Sectors in No Transfer Sector Read Cache< from Data Cache to Read Cache Yes - 652 642 Yes Sectors More in Data Cache Sectors in Data Yes Cache & RLA Sectors in Host Buffer < N Νo 646 No, N=Prefetch 644 Length Multi-Page Read m RLA Sectors (m <= No Sectors in Yes Parallelism) into ReadCache=N Data Cache